

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A method for configuring an integrated device in a first processor comprising:

~~decoding converting a memory configuration access corresponding to a memory address or Input Output (IO) address access~~ within a decoder of a second processor, the second processor coupled to the first processor, to a configuration cycle for configuration of the integrated device in the first processor;

~~routing the configuration cycle directly from the decoder to a chipset the first processor based at least in part on a routing information; and~~

~~forwarding the configuration cycle to configure the integrated device from an unconfigured state to a configured state.~~

Claim 2 (currently amended): The method of claim 1 wherein the configuration cycle is routed ~~to the chipset~~ via a network fabric.

Claim 3 (previously presented): The method of claim 2 wherein the network fabric is a plurality of point to point links.

Claim 4 (cancel)

Claim 5 (original): The method of claim 2 wherein the second processor is coupled to the first processor via the network fabric.

Claim 6 (currently amended): A method for configuring an integrated device in a first processor comprising:

~~decoding an Input Output (IO) configuration access within a second processor, coupled to [[a]] the first processor, to a configuration cycle, wherein the decoding includes retrieving a node~~

identifier and a port number using a configuration address associated with the IO configuration access; and

routing the configuration cycle including the node identifier and the port number directly from the second processor to the integrated device in the first processor based at least in part on a routing information to configure the integrated device from an unconfigured state to a configured state.

Claim 7 (cancel)

Claim 8 (currently amended): The method of claim [[7]] 6 wherein the network fabric is a plurality of point to point links.

Claim 9 (previously presented): The method of claim 6 wherein the configuration adheres to an interconnect of a predetermined protocol.

Claim 10 (previously presented): The method of claim 9 wherein the predetermined protocol comprises a PCI type interconnect protocol.

Claim 11 (currently amended): The method of claim [[7]] 6 wherein the second processor is coupled to the first processor via the network fabric.

Claim 12 (currently amended): A processor comprising:
a decoder to decode either a memory or IO configuration access for configuration of an integrated device of a second processor directly coupled to the processor to a configuration cycle, wherein the decoder is to receive a configuration address and provide a node identifier corresponding to an address range of the configuration address and a port identifier corresponding to a range of the node identifier with the configuration cycle; and
to transmit the configuration cycle directly to either a chipset or the integrated device.

Claim 13 (currently amended): The processor of claim 12 wherein the transmission of the configuration cycle to either the chipset or integrated device is via a PCI type interconnect.

Claim 14 (currently amended): The processor of claim 12 wherein the configuration cycle is to be routed to the integrated device ~~or chipset~~ via a network fabric.

Claim 15 (currently amended): A system comprising:
a first processor with a decoder coupled to a second network component with an integrated device, the decoder to decode either a memory or IO configuration access for configuration of the integrated device to a configuration cycle; and
to transmit the configuration cycle directly to either a chipset or the integrated device, wherein the configuration cycle adheres to a first type of interconnect protocol.

Claim 16 (previously presented): The system of claim 15 wherein the first type of interconnect protocol comprises a PCI type protocol.

Claim 17 (previously presented): The system of claim 15 wherein the configuration cycle is routed to the integrated device or the chipset via a network fabric.

Claim 18 (currently amended): An article of manufacture comprising:
a machine-readable storage medium having stored thereon a plurality of machine readable instructions, wherein when the instructions are executed by a system, the instructions provide configuration of an integrated device in a processor or network component by:
decoding either a memory or IO configuration access to a configuration cycle in a decoder of a second processor, wherein the decoder is to receive a configuration address and provide a node identifier corresponding to an address range of the configuration address and a port identifier corresponding to a range of the node identifier with the configuration cycle; and
transmitting the configuration cycle directly from the second processor to either a chipset or the integrated device, wherein the configuration cycle adheres to a first type of interconnect protocol.

Claim 19 (currently amended): The article of manufacture of claim 18 wherein the ~~chipset or~~ integrated device is coupled to [[a]] the decoder of ~~a~~ first the second processor coupled to the processor or network component via a network fabric.

Claim 20 (previously presented): The article of manufacture of claim 18 wherein the first type of interconnect protocol is in accordance with a PCI type protocol.

Claim 21 (cancel)